

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| S1 | 4 | YODER-MICHAEL-E.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 10:36 |
| S2 | 0 | YODER-MICHAEL.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 10:12 |
| S3 | 4 | ("5630088" "6598130").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 10:48 |
| S4 | 2160 | (711/147,127,132 714/5).ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:51 |
| S6 | 1222 | (memory near2 object) with (location locality) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 10:53 |
| S7 | 26 | (memory near2 object) with (locality) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 10:57 |
| S8 | 162 | (shared and stack) near2 object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:14 |
| S10 | 502 | fault adj handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:14 |

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|-----|-----|--|---|----|----|------------------|
| S12 | 3 | S8 and S10 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:27 |
| S13 | 88 | ((shared near2 memory) and (stack)) with object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:19 |
| S14 | 108 | ((shared near2 memory) and (stack)) and fault near2 handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:19 |
| S15 | 24 | ((shared near2 memory) and (stack)) and fault near2 handler and interleav\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:21 |
| S17 | 40 | determin\$3 with (memory near2 object) with (shared stack) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:22 |
| S21 | 27 | determin\$3 with (memory near2 object) with (shared) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:27 |
| S22 | 14 | determin\$3 with (memory near2 object) with (stack) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:28 |
| S23 | 1 | S21 and S22 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:28 |

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| S24 | 523 | determin\$3 with object with shared | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:39 |
| S25 | 731 | determin\$3 with object with stack | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:28 |
| S26 | 105 | determin\$3 with object with shared with memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:28 |
| S27 | 46 | determin\$3 with object with stack with memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:28 |
| S28 | 1 | S26 and S27 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:29 |
| S31 | 190 | determin\$3 with (memory adj object) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:31 |
| S32 | 12 | determin\$3 with (memory adj object) with comprises | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:32 |
| S33 | 0 | determin\$3 with (memory adj object) with comprises with shared | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:33 |

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|-----|-------|---|---|----|----|------------------|
| S35 | 17 | determin\$3 with (memory near2 object) with comprises | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:34 |
| S36 | 1 | determin\$3 with object with shared with comprises | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:38 |
| S37 | 5 | determin\$3 with object with stack with comprises | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:37 |
| S38 | 3 | determin\$3 with object with shared with compris\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:38 |
| S39 | 7 | (determin\$3 with object with shared) same interleav\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:48 |
| S40 | 21315 | (interleav\$3 interlac\$3 strip\$3) same (shared global common) same object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:49 |
| S41 | 883 | (interleav\$3 interlac\$3 strip\$3) with (shared global common) with object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:05 |
| S43 | 1 | S41 same (multiprocessor multi-processor SMP) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 11:52 |

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|-----|-----|--------------------------------|---|----|----|------------------|
| S46 | 70 | S41 and stack | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:52 |
| S48 | 1 | S41 and (fault near2 handler) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:00 |
| S49 | 9 | S41 and locality | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:02 |
| S50 | 280 | S41 and location | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:02 |
| S51 | 89 | S41 and cell | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:35 |
| S52 | 4 | "shared-memory object" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:36 |
| S53 | 78 | "shared memory object" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:36 |
| S54 | 2 | S41 and "shared memory object" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:36 |

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|-----|------|---|---|----|----|------------------|
| S55 | 8 | S41 and "shared memory" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:49 |
| S56 | 1 | "stack-type object" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:49 |
| S57 | 4 | "stack type object" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:49 |
| S58 | 1115 | "stack object" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:50 |
| S59 | 15 | "stack object" and "process stack" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:56 |
| S60 | 3224 | (711/147,127,132 714/5 718/104). ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:12 |
| S61 | 1 | S41 and S60 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:52 |
| S63 | 49 | thread near2 locality | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:57 |

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|-----|-----|-----------------------|---|----|----|------------------|
| S64 | 0 | S41 and S63 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:57 |
| S65 | 200 | object near2 locality | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:57 |
| S66 | 0 | S41 and S64 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 12:57 |
| S67 | 2 | S60 and S63 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 13:00 |
| S69 | 4 | S60 and S65 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 13:00 |

EAST Search History

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|-----|-------|---|---|----|----|------------------|
| S70 | 49 | (US-5892970-\$ or US-6240501-\$ or US-6308246-\$ or US-6871219-\$ or US-6073225-\$ or US-6026472-\$ or US-6035377-\$ or US-6035378-\$ or US-6088770-\$ or US-5613071-\$ or US-6044438-\$ or US-6092155-\$ or US-6115790-\$ or US-6122674-\$ or US-6122659-\$ or US-6148379-\$ or US-6286090-\$ or US-6343346-\$ or US-6351795-\$ or US-6446185-\$ or US-6546471-\$ or US-6618799-\$ or US-6427161-\$ or US-6226734-\$ or US-6199179-\$ or US-6247109-\$). did. or (US-6260068-\$ or US-6332180-\$ or US-6381682-\$ or US-6542926-\$ or US-6633916-\$ or US-6647508-\$ or US-5734922-\$ or US-5749095-\$ or US-5754877-\$ or US-5796605-\$ or US-5805839-\$ or US-5848254-\$ or US-5860159-\$ or US-5862316-\$ or US-5862357-\$ or US-5878268-\$ or US-5881303-\$ or US-5887138-\$ or US-5897657-\$ or US-5950226-\$ or US-5958019-\$ or US-5983326-\$ or US-6226671-\$). did. | USPAT | OR | ON | 2006/03/27 15:07 |
| S71 | 883 | (interleav\$3 interlac\$3 strip\$3) with (shared global common) with object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:05 |
| S72 | 0 | S70 and S71 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:12 |
| S74 | 21315 | (interleav\$3 interlac\$3 strip\$3) same (shared global common) same object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:06 |
| S75 | 0 | S70 and S74 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:06 |

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|-----|------|---|---|----|----|------------------|
| S76 | 1869 | (interleav\$3 interlac\$3 strip\$3) same (shared global common) same page | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:06 |
| S77 | 0 | S70 and S75 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:21 |
| S78 | 6 | S70 and (interleav\$3 interlac\$3 strip\$3) and (shared global common) and object | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:07 |
| S79 | 4880 | (711/147,127,132 709/213-216 714/5 718/104).cccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 18:58 |
| S80 | 2 | S79 and S71 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:31 |
| S81 | 11 | S70 and handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:21 |
| S82 | 0 | S70 and fault near2 handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 14:21 |
| S92 | 2 | (page adj fault) and (memory adj manager) and SMP | USPAT | OR | ON | 2006/03/27 15:59 |
| S93 | 0 | (fault adj handler) and SMP and node and (interleav\$3 interlac\$3 strip\$3) | USPAT | OR | ON | 2006/03/27 16:00 |

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| S96 | 77 | 711/200-209.ccls. and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:55 |
| S97 | 1 | (page adj fault) and (interleav\$3 interlac\$3 strip\$3) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:56 |
| S98 | 5 | (page adj fault) and handler and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:56 |
| S99 | 13 | (page adj fault) and (memory adj manager) and (interleav\$3 interlac\$3 strip\$3) and node | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:57 |
| S10 0 | 53 | (page adj fault) and (memory adj manager) and (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:58 |
| S10 1 | 20 | (page adj fault) and (memory adj manager) and (interleav\$3 interlac\$3 strip\$3) and handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:58 |
| S10 2 | 2 | (page adj fault) and (memory adj manager) and SMP adj node | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 15:59 |
| S10 4 | 2 | (fault adj handler) and SMP and node and (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 16:00 |

EAST Search History

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|----------|-----|--|---|----|----|------------------|
| S10 5 | 5 | (fault adj handler) and SMP and (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 16:10 |
| S10 6 | 10 | (fault adj handler) and SMP and local | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 16:02 |
| S10 7 | 172 | handler and SMP and (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 16:22 |
| S10 8 | 5 | ("6021479" "6105053").PN. OR ("6272612").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/03/27 16:13 |
| S10 9 | 120 | handler and SMP and (interleav\$3 interlac\$3 strip\$3) and node | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 16:22 |
| S11 3 | 2 | handler and (SMP with node) and (interleav\$3 interlac\$3 strip\$3) and switch\$3 and (memory adj manager) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/27 16:26 |
| S11 4 | 21 | handler and (SMP with node) and (interleav\$3 interlac\$3 strip\$3) and switch\$3 and manager | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 12:19 |

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|----------|-----|--|---|----|----|------------------|
| S11 5 | 60 | ("4843541" "4853843" "5237566" "5325517" "5392397" "5408649" "5414851" "5450570" "5471609" "5481707" "5481719" "5517648" "5537574" "5574914" "5583987" "5588111" "5606696" "5613146" "5625831" "5636341" "5640584" "5692193" "5717942" "5737763" "5765154" "5784702" "5819020" "5828894" "5838968" "5860115" "5884018" "5898870" "5923890" "5931938" "5950228" "5956522" "5987621" "6002851" "6012151" "6021508" "6035414" "6041377" "6047323" "6058423").PN. OR ("6381682").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/03/27 16:47 |
| S11 6 | 4 | handler and (SMP with node) and (interleav\$3 interlac\$3 strip\$3) and switch\$3 and manager and stack | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:04 |
| S11 7 | 1 | SMP same (interleav\$3 interlac\$3 strip\$3) same access adj time | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:38 |
| S11 8 | 345 | windows same "multi-threaded" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:08 |
| S11 9 | 54 | windows same "multi-threaded" and (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:08 |
| S12 0 | 6 | windows same "multi-threaded" and SMP and (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:09 |

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| S12 2 | 69 | thread with cell with run\$4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:40 |
| S12 3 | 8 | (thread with cell with run\$4) same (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:40 |
| S12 4 | 14 | (page adj fault) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 12:14 |
| S12 5 | 3 | (virtual memory manager) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 12:28 |
| S12 6 | 3 | (virtual memory manager) and (page adj fault) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 19:29 |
| S12 7 | 4 | (memory manager) and (page adj fault) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:19 |
| S12 8 | 0 | (memory manager) and handler and (interleav\$3 or interlac\$3 or strip\$3) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 12:42 |
| S12 9 | 0 | (manager) and handler and (interleav\$3 or interlac\$3 or strip\$3) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:07 |

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|----------|-----|---|---|-----|----|------------------|
| S13 1 | 332 | (access time) same (interleav\$3 or interlac\$3 or strip\$3) same (equal or "same") | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 16:04 |
| S13 3 | 42 | (access time) with (interleav\$3 or interlac\$3 or strip\$3) with (equal or "same") | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 16:03 |
| S13 4 | 8 | (access time) with (interleav\$3 or interlac\$3 or strip\$3) with (equal) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 16:03 |
| S13 5 | 76 | (access time) same (interleav\$3 or interlac\$3 or strip\$3) same (equal) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 16:04 |
| S13 7 | 13 | (access time) same (interleav\$3 or interlac\$3 or strip\$3) same (equal) same (CPU or processor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 16:05 |
| S13 8 | 2 | (manager) and interrupt and (interleav\$3 or interlac\$3 or strip\$3) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:07 |
| S13 9 | 14 | (memory manager) and (interrupt) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:09 |
| S14 0 | 2 | (memory manager) and (interrupt adj handler) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:08 |

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| S14 1 | 3 | (memory manager) and (interrupt with handler) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:09 |
| S14 2 | 3 | (memory manager) and (interrupt same handler) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:09 |
| S14 3 | 5 | (memory manager) and (handler) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:10 |
| S14 4 | 7 | (manager) and (handler) and 711/2.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:11 |
| S14 5 | 97 | (virtual memory manager) and (page adj fault) and handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:19 |
| S14 6 | 56 | (virtual memory manager) same (page adj fault) same handler | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:20 |
| S14 7 | 45 | (virtual memory manager) same (page adj fault) same handler same interrupt | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:20 |
| S14 8 | 8 | (virtual memory manager) same (page adj fault) same handler same interrupt and "711"/\$.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:31 |

EAST Search History

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|----------|------|---|---|-----|----|------------------|
| S14 9 | 9 | (virtual memory manager) same handler same interrupt and "711"/\$.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:32 |
| S15 0 | 18 | (virtual memory manager) same handler and "711"/\$.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 17:33 |
| S15 1 | 14 | (virtual memory manager) same handler and "711"/\$.ccls. and interrupt | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 18:56 |
| S15 3 | 415 | locality with (interleav\$3 interlac\$3 strip\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 18:57 |
| S15 4 | 4887 | (711/147,127,132 709/213-216 714/5 718/104).ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 18:58 |
| S15 5 | 4 | S153 and S154 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 18:58 |
| S15 6 | 34 | S153 and "711"/\$.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 19:05 |
| S15 7 | 0 | An Efficient Address Interleaving With Simultaneous Multiple Locality Options.ti. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2006/03/28 19:04 |

EAST Search History

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|----------|-----|---|---|-----|----|------------------|
| S15 8 | 2 | "6567900".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 19:05 |
| S16 0 | 260 | windows.clm. and "711"/\$.ccls. | USPAT | ADJ | ON | 2006/03/28 19:30 |
| S16 1 | 260 | "windows".clm. and "711"/\$.ccls. | USPAT | ADJ | ON | 2006/03/28 19:30 |
| S16 2 | 383 | "windows" and operating system. clm. and "711"/\$.ccls. | USPAT | ADJ | ON | 2006/03/28 19:31 |
| S16 3 | 49 | ("windows" and operating system). clm. and "711"/\$.ccls. | USPAT | ADJ | ON | 2006/03/28 19:32 |
| S16 4 | 1 | ("windows" and "tm").clm. and ""711""/\$.ccls. | USPAT | ADJ | ON | 2006/03/28 19:31 |
| S16 5 | 32 | ("windows" with operating system). clm. and "711"/\$.ccls. | USPAT | ADJ | ON | 2006/03/28 19:32 |



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Result # 1 Relevance: **ECM preprocessor or tracker using multi-processor modules (USH00000513)**
1988-08-02 IPCOM000000509D English (United States)
The tracking apparatus uses multi-processor modules for predicting in real time the parametric behaviour of radar signals to be jammed, as part of an electronic countermeasures system. The tracker system is partitioned into three board (module) types--(1) a subsystem request ...

Result # 2 Relevance: **Improved Store-Thru Cache**
1991-06-01 IPCOM000120870D English (United States)
This invention relates to a two-level cache hierarchy for a multi- processor (MP) system. Each processor (CP) has a private L1 cache, whereas the L2 cache is shared by all the (N) processors. For data which may be changed by any CP, a current copy is maintained in both its ...

Result # 3 Relevance: **What Can Be Automated?: The Computer Science and Engineering Research Study (COSERS)**
1980-01-01 IPCOM000128748D English (United States)
It is truly difficult to capture with a single question the essence of research in a diverse and very active area of science and technology, but the query in the title comes very close. This questions was first posed by the late Professor George Forsythe of Stanford ...

Result # 4 Relevance: **MTS The Michigan Terminal System VOLUME 4: TERMINALS AND TAPES Third Edition August 1974 Revised**
1974-09-01 IPCOM000128751D English (United States)
The software developed by the Computing Center staff for the operation of the 360/67 dual-processor computer can be described as a multi-processor supervisor which handles a number of resident, re-entrant programs. Among these is a large subsystem, called BTS (Michigan ...

Result # 5 Relevance:

A SYSTEM ORGANIZATION FOR APPLICATIVE PROGRAMMING

1981-12-31 IPCOM000128334D English (United States)
A distributed system for execution of programs written in a purely applicative (functional) language is described. The language inherently supports the representation of parallelism in computations, enhances programmability, and, due to its underlying algebraic framework, ...

Result # 6 Relevance: 

The Sync Model: A Parallel Execution Method for Logic Programming

1986-12-31 IPCOM000127952D English (United States)
The Sync Model, a parallel execution method for logic programming, is proposed. The Sync Model is a multiple-solution data-driven model that realizes AND- parallelism and OR-parallelism in a logic program assuming a message-passing mul- tiprocessor system. AND ...

Result # 7 Relevance: 

Sequential Program Prefetching in Memory Hierarchies

1978-12-01 IPCOM000131354D English (United States)
University of California, Berkeley Transfers of information between levels of an automatically managed memory hierarchy at the time the program references it (a miss) are usually costly in overhead operations and idle time. The fact that patterns of program execution and ...

Result # 8 Relevance: 

SUBMICRON SYSTEMS ARCHITECTURE

1986-12-31 IPCOM000127954D English (United States)
The central theme of this research is the architecture and design of VLSI sys-tems appropriate to a microcircuit technology scaled to submicron feature sizes. Our work is focused on VLSI architecture experiments that involve the design, construc-tion, programming, and use ...

Result # 9 Relevance: 

SUBMICRON SYSTEMS ARCHITECTURE ' Semiannual Technical Report

1986-12-31 IPCOM000127951D English (United States)
The central theme of this research is the architecture and design of VLSI systems appropriate to a microcircuit technology scaled to submicron feature sizes, and includes related efforts in concurrent computation and VLSI design. Additional background information can be ...

Result # 10 Relevance: 

The Torus Routing Chip

1986-12-31 IPCOM000127947D English (United States)
The torus routing chip (TRC) is a self-timed chip that performs deadlock-free cut-through routing in k-ary n-cube multiprocessor interconnection networks using a new method of deadlock avoidance called virtual channels. A prototype TRC with byte wide self-timed ...

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Search (interleaved or striped) and (shared or common or global) and (SMP or multiprocessor~ or query: multi-processor~) and (locality or location)

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Result # 11 Relevance:

MP Shaped Processor Memory

1983-03-01 IPCOM000045373D

The sharing of data in a multiprocessor (MP) environment creates difficulties. This article describes store-through approaches to sharing at the second level in a memory hierarchy.

English (United States)

Result # 12 Relevance:

Analytic Modeling of Computer Systems

1978-10-01 IPCOM000131247D

Duke University Deterministic and probabilistic models capable of representing more and more system parameters are being developed. One of their primary attractions is low cost. There are two major approaches to evaluating the performance of a computer system: simulation ...

English (United States)

Result # 13 Relevance:

Understanding and Exploiting Distribution

1985-12-31 IPCOM000128279D

In most workstation based distributed systems, distribution is limited to client-server distribution: server processes reside on dedicated server machines and clients access the servers over the network. In this research project we explore a more sophisticated form of ...

English (United States)

Result # 14 Relevance:

IEEE Computer Volume 15 Number 10 -- NEW PRODUCTS

1982-10-01 IPCOM000131543D

NEW PRODUCTS * CAD system operates without interface to host computer * System 80 models offer 4.8G bytes of disk storage * Computer care products assist in preventative * Multiplexers have expanded firmware options * Graphics kit for DEC VT.100 terminals is ...

English (United States)

Result # 15 Relevance:

Discussion: The Burroughs B 5000 in Retrospect

1987-01-01 IPCOM000129533D

Editor's Note: The day's discussion began with many of the key managers, designers, and developers

English (United States)

of the Burroughs B 5000 recalling the era in which the system was conceived and introduced.

Result # 16 Relevance: 

SUBMICRON SYSTEMS ARCHITECTURE

1988-12-31 IPCOM000127963D English (United States)
The central theme of this research is the architecture and design of VLSI systems appropriate to a microcircuit technology scaled to submicron feature sizes. Our work is focused on VLSI architecture experiments that involve the design, construction, programming, and use of ...

Result # 17 Relevance: 

SUBMICRON SYSTEMS ARCHITECTURE Semiannual Technical Report

1983-12-31 IPCOM000127930D English (United States)
The central theme of this research is the architecture and design of VLSI systems appropriate to a microcircuit technology scaled to submicron feature sizes, and includes related efforts in concurrent computation and VLSI design. Additional background information can be ...

Result # 18 Relevance: 

User Interface for a Parallel File System

1994-11-01 IPCOM000114196D English (United States)
Disclosed is a programming interface to a parallel file system. This interface allows application programs to access data stored on multiple disks and to control data layout.

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Search (interleaved or striped) and (shared or common or global) and (SMP or multiprocessor~ or query: multi-processor~) and (locality or location)

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|-----------------------|--|--|---------|
| #1 | ((default locality)<in>metadata) | | 0 |
| #2 | ((shared memory object)<in>metadata) | | 1 |
| #3 | ((shared memory object)<in>metadata) | | 1 |
| #4 | ((stack object)<in>metadata) | | 2 |
| #5 | ((stack-type object)<in>metadata) | | 0 |
| #6 | ((location or locality) <sentence> (interleaved memory))<in>metadata | | 2 |
| #7 | ((location or locality) <sentence> (striped memory))<in>metadata | | 0 |
| #8 | ((location or locality) <sentence> (striped))<in>metadata | | 15 |
| #9 | ((shared or global or common) and memory) <sentence> interleaved)<in>metadata | | 42 |
| #10 | ((shared or global or common) and memory) <sentence> interleaved) and smp)<in>metadata | | 0 |
| #11 | ((shared or global or common) and memory) <sentence> interleaved) and (multiprocessor or multi-processor))<in>metadata | | 14 |

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|---------------------|--|----|
| #12 | (((shared or global or common) and memory) <sentence> interleaved) and (multi-processor or multi-processor) and (stack)) <in>metadata) | 0 |
| #13 | (((shared or global or common) and memory and interleaved and (multi-processor or multi-processor))<in>metadata) | 18 |
| #14 | (((shared or global or common) and memory and interleaved and (multi-processor or multi-processor) and stack) <in>metadata) | 0 |



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